

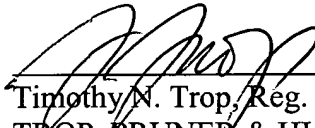
## REMARKS

As amended, the first and second portions are scaled so that they can be stored entirely within the first memory area. As indicated, Michelson teaches away since he teaches filling up lines within the VRAM, but he does so by wrapping around from line to line. This creates the problem which Michelson discusses in column 7, lines 18-22. By scaling appropriately to prevent running over the line, as now set forth in the claims, an advantageous structure results relative to that described in Michelson.

Therefore, the application should now be in condition for allowance.

Respectfully submitted,

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Timothy N. Trop, Reg. No. 28,994  
TROP, PRUNER & HU, P.C.  
8554 Katy Freeway, Ste. 100  
Houston, TX 77024  
713/468-8880 [Phone]  
713/468-8883 [Fax]

Attorneys for Intel Corporation